

Δημοσιεύσεις

Παναγιώτης Αλεφραγκής Καθηγητής Εφαρμογών

Σύνολο δημοσιεύσεων: **26**

2018 (4)

- Andreas Emeretlis, George Theodoridis, Panayiotis Alefragis and Nikolaos Voros, *Static Mapping of Applications on Heterogeneous Multi-Core Platforms Combining Logic-Based Benders Decomposition with Integer Linear Programming*, **ACM Transactions on Design Automation of Electronic Systems**, ACM, 2018
- Panayiotis Alefragis, George Theodoridis, Merkourios Katsimpris, Christos Valouxis, Christos Gogos, George Goulas, Nikolaos S. Voros, *Mapping and scheduling hard real-time applications on multicore systems - The ARGO approach*, **14th International Symposium on Applied Reconfigurable Computing**, 700 - 711, May 2nd - 4th, Santorini Island, Greece, 2018
- Christos Valouxis, Christos Gogos, Panayiotis Alefragis, George Theodoridis and Nikolaos Voros, *Constraint Programming Modeling for the Task Scheduling Problem with Data Storage at MPSoCs*, **12th International Conference of the Practice and Theory of Automated Timetabling (PATAT 2018)**, August 28 - 31, Vienna, Austria, 2018
- Andreas Emeretlis, Theodore Tsakoulis, George Theodoridis, Panayiotis Alefragis, and Nikolaos Voros, *Task graph mapping and scheduling on heterogeneous architectures under communication constraints*, **Proceedings of the International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation (SAMOS 2017)**, July 17 - 20, Samos, Greece, 2018

2017 (2)

- Steven Derrien, Isabelle Puaut, Panayiotis Alefragis, Marcus Bednara, Harald Bucher, Clément David, Yann Debray, Umut Durak, Imen Fassi, Christian Ferdinand, Damien Hardy, Angeliki Kritikakou, Gerard Rauwerda, Simon Reder, Martin Sicks, Timo Stripf, Kim Sunesen, Timon ter Braak, Nikolaos Voros and Jürgen Becker, *WCET-Aware Parallelization of Model-Based Applications for Multicores: the ARGO Approach*, **Conference on Design, Automation and Test in Europe (DATE)**, March 27th - 31st, Lausanne, Switzerland, 2017
- Andreas Emeretlis, George Theodoridis, Panayiotis Alefragis and Nikolaos Voros, *Exploration of Logic-Based Benders Decomposition Approach for Mapping Applications on Heterogeneous Multi-Core Platforms*, **Multi-disciplinary scheduling International Conference (MISTA 2017)**, December 5th - 8th, Kuala Lumpur, Malaysia, 2017

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- Andreas Emeretlis, George Theodoridis, Panayiotis Alefragis, Nikolaos Voros, *Improvements of a hybrid ILP-CP Benders decomposition for mapping and scheduling task DAGs on heterogeneous architectures*, **Proceedings of 11th Practice and Theory of Automated Timetabling (PATAT 2016)**, August 23th - 26th, Udine, Italy, 2016
- Christos Valouxis, Christos Gogos, Panayiotis Alefragis, George Goulas, Nikolaos Voros, Efthymios Housos, *Scheduling Independent Tasks on Heterogeneous Processors using Heuristics and Column Pricing*, **Future Generation Computer Systems, The International Journal of eScience**, Elsevier, 2016
- Andreas Emeretlis, George Theodoridis, Panayiotis Alefragis, Nikolaos Voros, *A Logic-Benders Decomposition Approach for Mapping Applications on Heterogeneous Multicore Platforms*, **ACM Transactions on Embedding Computing Systems**, Volume 15 Issue 1, Article No. 19, ACM, 2016
- A. Emeretlis, G. Theodoridis, P. Alefragis, N. Voros, *A Hybrid Approach for Mapping and Scheduling on Heterogeneous Multicore Systems*, **4th Workshop on Virtual Prototyping of Parallel and Embedded Systems (ViPES 2016)**, July 17th, Samos Island, Greece, 2016
- Umut Durak, David Müller, Jürgen Becker, Nikolaos S. Voros, Panayiotis Alefragis, Timo Stripf, Pierre-Aimé Agnel, Gerard Rauwerda, Kim Sunesen, *Model-based Development of Enhanced Ground Proximity Warning System for Heterogeneous Multicore Architectures*, **23rd Symposium on Simulation Techniques (ASIM 2016), Special Interest Track Modelling and Simulation in Aviation**, September 7th - 9th, Dresden, Germany, 2016

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- A. Emeretlis, G. Theodoridis, P. Alefragis, N. S. Voros, *Mapping DAGs on Heterogeneous Platform using Logic-Based Benders Decomposition*, **Proceedings of IEEE Annual Symposium on ISVLSI**, pp. 119 - 124, July 8th - 10th, Montpellier, France, 2015

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- Juergen Becker, Thomas Bruckschloegl, Oliver Oey, Timo Stripf, George Goulas, Nick Raptis, Christos Valouxis, Panayiotis Alefragis, Nikolaos S. Voros, Christos Gogos, *Profile Guided Compilation of Scilab Algorithms for Multiprocessor Systems*, **Reconfigurable Computing: Architectures, Tools, and Applications 10th International Symposium on Applied Reconfigurable Computing (ARC 2014)**, pp. 330-336, Vilamoura, Algarve, April 14th - 16th, Portugal, Springer, Lecture Notes in Computer Science, 2014
- Andreas Emeretlis, George Theodoridis, Panayiotis Alefragis, Nikolaos Voros, *A hybrid ILP-CP model for mapping Directed Acyclic Task Graphs to multicore architectures*, **Proceedings of the 28th IEEE International Parallel & Distributed Processing Symposium (IPDPSW 2014)**, Phoenix, Arizona, May 19th - 23th, USA, 2014
- Oliver Oey, Michael Rueckauer, Timo Stripf, Jurgen Becker, George Goulas, Panayiotis Alefragis, Nikolaos Voros, Ali El Moussawi, Steven Derrien, *From Scilab To High Performance Embedded Multicore Systems - The ALMA Approach*, **Proceedings of 16th ACM Software and Compilers for Embedded Systems (SCOPES) Workshop, June 10th - 11th**,

Schloss Rheinfels, St. Goar, Germany, June 10th – 11th, Schloss Rheinfels, St. Goar, Germany, 2014

- Panayiotis Alefragis, Christos Gogos, Christos Valouxis, George Goulas, Nikolaos Voros, Efthymios Housos,, *Assigning and Scheduling Hierarchical Task Graphs to Heterogenous Resources*, **Proceedings of 10th Practice and Theory of Automated Timetabling (PATAT 2014)**, August 26th – 29th, York, United Kingdom, 2014

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- George Goulas, Christos Valouxis, Panayiotis Alefragis, Nikolaos S. Voros, Christos Gogos, Oliver Oey, Timo Stripf, Thomas Bruckschloegl, Juergen Becker, *Coarse-grain Optimization and Code Generation for Embedded Multicore Systems*, **Proceedings of 16th Euromicro Conference on Digital System Design (DSD 2013)**, September 4 - 6, Santander, Spain, 2013
- George Goulas, Christos Gogos, Christos Valouxis, Panayiotis Alefragis and Nikolaos Voros, *Coarse grained parallelism optimization for multicore architectures: The ALMA project approach*, **9th International Symposium on Applied Reconfigurable Computing**, P. Brisk, J.G. de Figueiredo Coutinho, P.C. Diniz (Eds.): ARC 2013, LNCS 7806, pp. 235–236, March 25th - 27th, Los Angeles, United States, Springer-Verlag Berlin Heidelberg, 2013
- George Goulas, Christos Gogos, Christos Valouxis, Panayiotis Alefragis, Nikolaos S. Voros, *Coarse grain parallelization using Integer Programming*, **11th IEEE International Conference on Industrial Informatics**, July 29th - 31st, Bochum, Germany, 2013
- George Goulas, Christos Valouxis, Panayiotis Alefragis, Nikolaos S. Voros, Christos Gogos, Oliver Oey, Timo Stripf, Thomas Bruckschloegl, Juergen Becker, *Coarse-grain Optimization and Code Generation for Embedded Multicore Systems*, **Proceedings of 16th Euromicro Conference on Digital System Design**, September 4st - 6th, Santander, Spain, 2013
- Timo Stripf, Oliver Oey, Thomas Bruckschloegl, Juergen Becker, Gerard Rauwerda, Kim Sunesen, George Goulas, Panayiotis Alefragis, Nikolaos S. Voros, Steven Derrien, Olivier Sentieys, Nikolaos Kavvadias, Grigoris Dimitroulakos, Kostas Masselos, Dimitrios Kritharidis, Nikolaos Mitas, Thomas Perschke, *Compiling Scilab To High Performance Embedded Multicore Systems*, **Journal of Microprocessors and Microsystems**, Elsevier, 2013
- Christos Valouxis, Christos Gkogkos, Panayiotis Alefragis, George Goulas, Nikolaos Voros, Efthymios Housos, *DAG Scheduling using Integer Programming in heterogeneous parallel execution environments*, **Proceedings of 6th Multidisciplinary International Conference on Scheduling**, August 27th – 30rd, Ghent, Belgium, Belgium, 2013

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- Timo Stripf, Oliver Oey, Juergen Becker, Gerard Rauwerda, Kim Sunesen, George Goulas, Panayiotis Alefragis, Nikolaos S. Voros, Diana Goehringer, Michael Huebner, Steven Derrien, Daniel Menard, Olivier Sentieys, Nikolaos Kavvadias, Grigoris Dimitroulakos, Kostas Masselos, Dimitrios Kritharidis, Nikolaos Mitas, *A Flexible Approach for Compiling SciLab to Reconfigurable Multicore Embedded Systems*, **7th International Workshop on**

Reconfigurable Communication-centric Systems-on-Chip, pp. 1 - 8, July 9th to 11th, York, United Kingdom, 2012

- George Goulas, Panayiotis Alefragis, Nikolaos S. Voros, Christos Valouxis, Christos Gogos, Nikolaos Kavvadias, Grigoris Dimitroulakos, Kostas Masselos, Diana Goehring, Steven Derrien, Daniel Ménard, Olivier Sentieys, Michael Huebner, Timo Stripf, Juergen Becker, Gerard Rauwerda, Kim Sunesen, Dimitrios Kritharidis, Nikolaos Mitas, *From SciLab to Multicore Embedded Systems: Algorithms and Methodologies*, **Proceedings of SAMOS XII** □ **International Conference on Embedded Computing Systems: Architectures, Modelling and Simulation**, pp. 268 - 275, July 16th to 19th, Samos, Greece, 2012
- Timo Stripf, Michael Huebner, Juergen Becker, Gerard Rauwerda, Kim Sunesen, George Goulas, Panayiotis Alefragis, Nikolaos S. Voros, Diana Goehring, Steven Derrien, Daniel Menard, Olivier Sentieys, Nikolaos Kavvadias, Kostas Masselos, Dimitrios Kritharidis, *From Scilab To High Performance Embedded Multicore Systems* □ *The ALMA Approach*, **Euromicro Conference on Digital System Design (DSD 2012)**, pp. 114 - 121, September 5th - 8th, Cesme, Izmir, Turkey, 2012